

\*\*\* SPARE PART\*\*\* SIMATIC S7-300, CPU 314 CPU WITH INTEGRATED 24 V DC POWER SUPPLY 24 KBYTE WORKING MEMORY

### Supply voltage

Rated value (DC)	24 V
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V

### Input current

Current consumption (rated value)	1 000 mA
Inrush current, typ.	8 A

### Power loss

Power loss, max.	8 W
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### Memory

Work memory	
<ul style="list-style-type: none"> <li>integrated</li> </ul>	24 kbyte; 24 KB/8 K instructions RAM (integrated); 1 instruction means 3 bytes on average
Load memory	
<ul style="list-style-type: none"> <li>expandable FEPRM</li> </ul>	Yes; Flash-EPRM
<ul style="list-style-type: none"> <li>expandable FEPRM, max.</li> </ul>	4 Mbyte
<ul style="list-style-type: none"> <li>integrated RAM, max.</li> </ul>	40 kbyte
Backup	
<ul style="list-style-type: none"> <li>present</li> </ul>	Yes
<ul style="list-style-type: none"> <li>with battery</li> </ul>	Yes; all blocks
<ul style="list-style-type: none"> <li>without battery</li> </ul>	Yes; 4 KB: bit memory, counter, times and data

### CPU processing times

for bit operations, typ.	0.3 $\mu$ s
for bit operations, max.	0.6 $\mu$ s
for word operations, typ.	1 $\mu$ s
for fixed point arithmetic, typ.	2 $\mu$ s
for floating point arithmetic, typ.	50 $\mu$ s
for timer/counter operations, typ.	12 $\mu$ s

### CPU-blocks

DB	
<ul style="list-style-type: none"> <li>Number, max.</li> </ul>	127
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	8 kbyte
FB	

• Number, max.	128
• Size, max.	8 kbyte
<b>FC</b>	
• Number, max.	128
• Size, max.	8 kbyte
<b>OB</b>	
• Description	see instruction list
• Size, max.	8 kbyte
• Number of free cycle OBs	1; OB 1
• Number of time alarm OBs	1; OB 10
• Number of cyclic interrupt OBs	1; OB 35
• Number of process alarm OBs	1; OB 40
• Number of startup OBs	1; OB 100
<b>Nesting depth</b>	
• per priority class	8
<b>Counters, timers and their retentivity</b>	
<b>S7 counter</b>	
• Number	64
of which retentive with battery	
— can be set	Yes
— lower limit	0
— upper limit	63
of which retentive without battery	
— can be set	Yes
— lower limit	0
— upper limit	63
<b>Counting range</b>	
— lower limit	1
— upper limit	999
<b>S7 times</b>	
• Number	128
of which retentive with battery	
— adjustable	Yes
— lower limit	0
— upper limit	127
of which retentive without battery	
— adjustable	Yes
— lower limit	0
— upper limit	127
<b>Time range</b>	
— lower limit	10 ms

— upper limit

9 990 s

## Data areas and their retentivity

### Flag

- |                                      |   |
|--------------------------------------|---|
| • Number, max.                       | 256 byte                                  |
| • Retentivity available              | Yes; MB 0 to MB 255                       |
| • of which retentive with battery    | 0 to 2 047 (M 0.0 to M 255.7, adjustable) |
| • of which retentive without battery | 0 to 2 047 (M 0.0 to M 255.7, adjustable) |

## Address area

### I/O address area

- |           |          |
|-----------|----------|
| • Inputs  | 512 byte |
| • Outputs | 512 byte |

### Process image

- |           |          |
|-----------|----------|
| • Inputs  | 128 byte |
| • Outputs | 128 byte |

### Digital channels

- |           |       |
|-----------|-------|
| • Inputs  | 1 024 |
| • Outputs | 1 024 |

### Analog channels

- |           |     |
|-----------|-----|
| • Inputs  | 256 |
| • Outputs | 128 |

### Addressing volume

- |           |          |
|-----------|----------|
| • Inputs  | 122 byte |
| • Outputs | 122 byte |

## Hardware configuration

- |                                     |   |
|-------------------------------------|---|
| Number of expansion units, max.     | 3   |
| connectable programming devices/PCs | PGs/PCs with STEP 7 connectable via MPI interface |

- |  |    |
|--|----|
| Number of modules per DP slave interface, max. | 16 |
|--|----|

### Number of DP masters

- |              |             |
|--------------|-------------|
| • integrated | 0           |
| • via CP     | 1; CP 342-5 |

### Number of operable FMs and CPs (recommended)

- |           |   |
|-----------|---|
| • FM      | 4 |
| • CP, PtP | 2 |
| • CP, LAN | 1 |

### Rack

- |                          |    |
|--------------------------|----|
| • Modules per rack, max. | 32 |
|--------------------------|----|

## Time of day

### Clock

- |                              |     |
|------------------------------|-----|
| • Hardware clock (real-time) | Yes |
|------------------------------|-----|

## Interfaces

<b>MPI</b>	
<ul style="list-style-type: none"> <li>• Cable length, max.</li> </ul>	9 100 m; without repeaters: 50 m; with 2 repeaters: 1100 m; with 10 repeaters in series: 9100 m; via fiber optic cable: 23.8 km (with 16 star hubs or OLMs)
<b>1. Interface</b>	
<b>Functionality</b>	
<ul style="list-style-type: none"> <li>• MPI</li> </ul>	Yes
<b>MPI</b>	
<ul style="list-style-type: none"> <li>• Number of nodes, max.</li> </ul>	32; 32 nodes on MPI bus; PG/PC, OP, additional S7-300/400, C7; per CPU max. 4 static and 4 dynamic connections
<ul style="list-style-type: none"> <li>• Transmission rate, max.</li> </ul>	187.5 kbit/s
<b>Services</b>	
— PG/OP communication	Yes
— Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes
<b>Communication functions</b>	
PG/OP communication	Yes
<b>Global data communication</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes
<b>S7 basic communication</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes
<b>S7 communication</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• as server</li> </ul>	Yes
<b>S5 compatible communication</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes; via loadable blocks
<b>Standard communication (FMS)</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes; via loadable blocks
<b>Number of connections</b>	
<ul style="list-style-type: none"> <li>• overall</li> </ul>	
— of which dynamic	8
— of which static	4
<b>Configuration</b>	
<b>Configuration software</b>	
<ul style="list-style-type: none"> <li>• STEP 7</li> </ul>	Yes; V5.0, V5.0 SP1
<b>Programming</b>	
<ul style="list-style-type: none"> <li>• Command set</li> </ul>	Binary logic operations, bracketed operations, result allocation, saving, counting, loading, transferring, comparing, shifting, rotating, complementation, calling blocks, fixed point arithmetic, floating point arithmetic, jump functions
<ul style="list-style-type: none"> <li>• Nesting levels</li> </ul>	8

<ul style="list-style-type: none"> <li>• Program organization</li> <li>• System functions (SFC)</li> </ul>	Linear, structured Interrupt and error processing, copy data, clock functions, diagnostic functions, module parameterization, operating mode transitions
<ul style="list-style-type: none"> <li>• System function blocks (SFB)</li> </ul>	1
<b>Programming language</b>	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— GRAPH	Yes
<b>Software libraries</b>	
— Process diagnostics	Yes
— Software controller	Yes; depending on the required memory space and the resulting execution time
<b>Know-how protection</b>	
<ul style="list-style-type: none"> <li>• User program protection/password protection</li> </ul>	Yes
<b>Cycle time monitoring</b>	
<ul style="list-style-type: none"> <li>• lower limit</li> <li>• upper limit</li> <li>• adjustable</li> <li>• preset</li> </ul>	1 ms 6 000 ms Yes 150 ms
<b>Dimensions</b>	
Width	80 mm
Height	125 mm
Depth	130 mm
<b>Weights</b>	
Weight, approx.	530 g; Memory card 16 g
<b>last modified:</b>	03/23/2017