

\*\*\*\*\* Replacement part \*\*\*\*\* SIMATIC S7-400, CPU 414-3 Central processing unit with: work memory 1.4 MB, (700 KB code, 700 KB data), 1st interface MPI/DP 12 Mbit/s, 2nd interface PROFIBUS DP, 3rd interface plug-in IFM module

General information	
Product type designation	CPU 414-3
Firmware version	V4.0
Product function	
<ul style="list-style-type: none"> <li>• Isochronous mode</li> </ul>	Yes
Engineering with	
<ul style="list-style-type: none"> <li>• Programming package</li> </ul>	STEP 7 V5.2 SP1 HF3 or higher with HW update
CiR - Configuration in RUN	
CiR synchronization time, basic load	100 ms
CiR synchronization time, time per I/O byte	80 µs
Supply voltage	
Rated value (DC)	Power supply via system power supply
Input current	
from backplane bus 5 V DC, typ.	1 A
from backplane bus 5 V DC, max.	1.2 A
from backplane bus 24 V DC, max.	Total current consumption of the components connected to the MPI/DP interfaces, but no more than 150 mA per interface
Power loss	
Power loss, typ.	4.5 W
Memory	
Type of memory	RAM
Work memory	
<ul style="list-style-type: none"> <li>• integrated</li> <li>• integrated (for program)</li> <li>• integrated (for data)</li> <li>• expandable</li> </ul>	1.4 Mbyte 700 kbyte 700 kbyte No
Load memory	
<ul style="list-style-type: none"> <li>• expandable FEPR0M</li> <li>• expandable FEPR0M, max.</li> <li>• integrated RAM, max.</li> <li>• expandable RAM</li> <li>• expandable RAM, max.</li> </ul>	Yes; with Memory Card (FLASH) 64 Mbyte 256 kbyte Yes; with Memory Card (RAM) 16 Mbyte
Backup	
<ul style="list-style-type: none"> <li>• present</li> <li>• with battery</li> <li>• without battery</li> </ul>	Yes Yes; all data No
Battery	
Backup battery	
<ul style="list-style-type: none"> <li>• Backup current, typ.</li> <li>• Backup current, max.</li> <li>• Backup time, max.</li> <li>• Feeding of external backup voltage to CPU</li> </ul>	550 µA 1 530 µA 144 d 5 V DC to 15 V DC
CPU processing times	
for bit operations, typ.	0.06 µs
for word operations, typ.	0.06 µs
for fixed point arithmetic, typ.	0.06 µs
for floating point arithmetic, typ.	0.18 µs
CPU-blocks	
DB	

<ul style="list-style-type: none"> <li>• Number, max.</li> <li>• Size, max.</li> </ul>	4 095; DB 0 reserved 64 kbyte
<b>FB</b>	
<ul style="list-style-type: none"> <li>• Number, max.</li> <li>• Size, max.</li> </ul>	2 048 64 kbyte
<b>FC</b>	
<ul style="list-style-type: none"> <li>• Number, max.</li> <li>• Size, max.</li> </ul>	2 048 64 kbyte
<b>OB</b>	
<ul style="list-style-type: none"> <li>• Number, max.</li> <li>• Size, max.</li> <li>• Number of time alarm OBs</li> <li>• Number of delay alarm OBs</li> <li>• Number of cyclic interrupt OBs</li> <li>• Number of process alarm OBs</li> <li>• Number of multicomputing OBs</li> </ul>	see instruction list 64 kbyte 4 4 4 4 1
<b>Nesting depth</b>	
<ul style="list-style-type: none"> <li>• per priority class</li> <li>• additional within an error OB</li> </ul>	24 1
<b>Counters, timers and their retentivity</b>	
<b>S7 counter</b>	
<ul style="list-style-type: none"> <li>• Number</li> </ul>	2 048
<b>Retentivity</b>	
<ul style="list-style-type: none"> <li>— adjustable</li> <li>— preset</li> </ul>	Yes Z 0 to Z 7
<b>Counting range</b>	
<ul style="list-style-type: none"> <li>— lower limit</li> <li>— upper limit</li> </ul>	0 999
<b>IEC counter</b>	
<ul style="list-style-type: none"> <li>• present</li> <li>• Type</li> </ul>	Yes SFB
<b>S7 times</b>	
<ul style="list-style-type: none"> <li>• Number</li> </ul>	2 048
<b>Retentivity</b>	
<ul style="list-style-type: none"> <li>— adjustable</li> <li>— preset</li> </ul>	Yes No times retentive
<b>Time range</b>	
<ul style="list-style-type: none"> <li>— lower limit</li> <li>— upper limit</li> </ul>	10 ms 9 990 s
<b>IEC timer</b>	
<ul style="list-style-type: none"> <li>• present</li> <li>• Type</li> </ul>	Yes SFB
<b>Data areas and their retentivity</b>	
Retentive data area (incl. timers, counters, flags), max.	Total working and load memory (with backup battery)
<b>Flag</b>	
<ul style="list-style-type: none"> <li>• Size, max.</li> <li>• Retentivity available</li> <li>• Retentivity preset</li> <li>• Number of clock memories</li> </ul>	8 kbyte Yes; From MB 0 to MB 8 191 MB 0 to MB 15 8; 1 memory byte
<b>Local data</b>	
<ul style="list-style-type: none"> <li>• adjustable, max.</li> <li>• preset</li> </ul>	16 kbyte 8 kbyte
<b>Address area</b>	
<b>I/O address area</b>	
<ul style="list-style-type: none"> <li>• Inputs</li> <li>• Outputs</li> </ul>	8 kbyte 8 kbyte
<b>Process image</b>	
<ul style="list-style-type: none"> <li>• Inputs, adjustable</li> <li>• Outputs, adjustable</li> <li>• Inputs, default</li> </ul>	8 kbyte 8 kbyte 256 byte

<ul style="list-style-type: none"> <li>• Outputs, default</li> </ul>	256 byte
<ul style="list-style-type: none"> <li>• consistent data, max.</li> </ul>	244 byte
<ul style="list-style-type: none"> <li>• Access to consistent data in process image</li> </ul>	Yes
<b>Subprocess images</b>	
<ul style="list-style-type: none"> <li>• Number of subprocess images, max.</li> </ul>	15
<b>Digital channels</b>	
<ul style="list-style-type: none"> <li>• Inputs</li> </ul>	65 536
<ul style="list-style-type: none"> <li> <ul style="list-style-type: none"> <li>— of which central</li> </ul> </li> </ul>	65 536
<ul style="list-style-type: none"> <li>• Outputs</li> </ul>	65 536
<ul style="list-style-type: none"> <li> <ul style="list-style-type: none"> <li>— of which central</li> </ul> </li> </ul>	65 536
<b>Analog channels</b>	
<ul style="list-style-type: none"> <li>• Inputs</li> </ul>	4 096
<ul style="list-style-type: none"> <li> <ul style="list-style-type: none"> <li>— of which central</li> </ul> </li> </ul>	4 096
<ul style="list-style-type: none"> <li>• Outputs</li> </ul>	4 096
<ul style="list-style-type: none"> <li> <ul style="list-style-type: none"> <li>— of which central</li> </ul> </li> </ul>	4 096
<b>Hardware configuration</b>	
Number of expansion units, max.	21; of which 6 ER with K-bus
connectable OPs	31 without message processing, 8 with message processing
Multicomputing	Yes; 4 CPUs max. (with UR1 or UR2)
<b>Interface modules</b>	
<ul style="list-style-type: none"> <li>• Number of connectable IMs (total), max.</li> </ul>	6
<ul style="list-style-type: none"> <li>• Number of connectable IM 460s, max.</li> </ul>	6
<ul style="list-style-type: none"> <li>• Number of connectable IM 463s, max.</li> </ul>	4; IM 463-2
<b>Number of DP masters</b>	
<ul style="list-style-type: none"> <li>• integrated</li> </ul>	2
<ul style="list-style-type: none"> <li>• via CP</li> </ul>	10; CP 443-5 Extended
<ul style="list-style-type: none"> <li>• via IM 467</li> </ul>	4
<ul style="list-style-type: none"> <li>• Mixed mode IM + CP permitted</li> </ul>	No; IM 467 cannot be used jointly with CP 443-5 Ext.
<ul style="list-style-type: none"> <li>• via interface module</li> </ul>	1; IF 964-DP
<ul style="list-style-type: none"> <li>• Number of pluggable S5 modules (via adapter capsule in central device), max.</li> </ul>	6
<b>Number of operable FMs and CPs (recommended)</b>	
<ul style="list-style-type: none"> <li>• FM</li> </ul>	Limited by number of slots and number of connections
<ul style="list-style-type: none"> <li>• CP, PtP</li> </ul>	CP 440: Limited by number of slots; CP 441: limited by number of connections
<ul style="list-style-type: none"> <li>• CP, LAN</li> </ul>	Limited by number of slots and number of connections
<ul style="list-style-type: none"> <li>• PROFIBUS and Ethernet CPs</li> </ul>	14; incl. CP 443-5 Ext. and IM 467
<b>Slots</b>	
<ul style="list-style-type: none"> <li>• required slots</li> </ul>	2
<b>Time of day</b>	
<b>Clock</b>	
<ul style="list-style-type: none"> <li>• Hardware clock (real-time)</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• retentive and synchronizable</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• Resolution</li> </ul>	1 ms
<ul style="list-style-type: none"> <li>• Deviation per day (buffered), max.</li> </ul>	1.7 s; Power on
<ul style="list-style-type: none"> <li>• Deviation per day (unbuffered), max.</li> </ul>	8.6 s; Power off
<b>Operating hours counter</b>	
<ul style="list-style-type: none"> <li>• Number</li> </ul>	8
<ul style="list-style-type: none"> <li>• Number/Number range</li> </ul>	0 to 7
<ul style="list-style-type: none"> <li>• Range of values</li> </ul>	0 to 32767 hours
<ul style="list-style-type: none"> <li>• Granularity</li> </ul>	1 h
<ul style="list-style-type: none"> <li>• retentive</li> </ul>	Yes
<b>Clock synchronization</b>	
<ul style="list-style-type: none"> <li>• supported</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• to MPI, master</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• on MPI, device</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• to DP, master</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• on DP, device</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• in AS, master</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• in AS, device</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• to IF 964 DP</li> </ul>	Yes; as Master or Slave

## 1. Interface

Interface type	MPI/PROFIBUS DP
Isolated	Yes
<b>Interface types</b>	
• RS 485	Yes
• Output current of the interface, max.	150 mA
<b>Protocols</b>	
• MPI	Yes
• PROFIBUS DP master	Yes
• PROFIBUS DP device	Yes
<b>MPI</b>	
• Number of connections	32
• Transmission rate, max.	12 Mbit/s
<b>Services</b>	
— PG/OP communication	Yes
— Routing	Yes
— Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes
<b>PROFIBUS DP master</b>	
• Number of connections, max.	16
• Transmission rate, max.	12 Mbit/s
• max. number of DP devices	32
<b>Services</b>	
— PG/OP communication	Yes
— Routing	Yes
— S7 basic communication	Yes
— S7 communication	Yes
— Equidistance	Yes
— SYNC/FREEZE	Yes
— activation/deactivation of DP devices	Yes
— Direct data exchange (slave-to-slave communication)	Yes
<b>Address area</b>	
— Inputs, max.	2 kbyte
— Outputs, max.	2 kbyte
<b>User data per DP device</b>	
— Inputs, max.	244 byte
— Outputs, max.	244 byte
— Slots, max.	244
— per slot, max.	128 byte
<b>1st interface / PROFIBUS DP device / header</b>	
• Number of connections	16
• GSD file	<a href="http://www.siemens.com/profibus-gsd">http://www.siemens.com/profibus-gsd</a>
• Transmission rate, max.	12 Mbit/s
• Address area, max.	32
• User data per address area, max.	32 byte
— of which consistent, max.	32 byte
<b>Services</b>	
— PG/OP communication	Yes
— Routing	Yes
<b>Transfer memory</b>	
— Inputs	244 byte
— Outputs	244 byte
<b>2. Interface</b>	
Interface type	PROFIBUS DP
Isolated	Yes
<b>Interface types</b>	
• RS 485	Yes
• Output current of the interface, max.	150 mA
<b>Protocols</b>	
• PROFIBUS DP master	Yes

• PROFIBUS DP device	Yes
<b>PROFIBUS DP master</b>	
• Number of connections, max.	16
• Transmission rate, max.	12 Mbit/s
• max. number of DP devices	96
<b>Services</b>	
— PG/OP communication	Yes
— Routing	Yes
— Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes
— Equidistance	Yes
— SYNC/FREEZE	Yes
— activation/deactivation of DP devices	Yes
— Direct data exchange (slave-to-slave communication)	Yes
<b>Address area</b>	
— Inputs, max.	6 kbyte
— Outputs, max.	6 kbyte
<b>User data per DP device</b>	
— Inputs, max.	244 byte
— Outputs, max.	244 byte
— Slots, max.	244
— per slot, max.	128 byte
<b>2nd interface / PROFIBUS DP device / header</b>	
• Transmission rate, max.	12 Mbit/s
• Address area, max.	32
• User data per address area, max.	32 byte
— of which consistent, max.	32 byte
<b>Services</b>	
— Routing	Yes
<b>Transfer memory</b>	
— Inputs	244 byte
— Outputs	244 byte
<b>3. Interface</b>	
Interface type	pluggable interface module (IF), technical data as for 2nd interface
Plug-in interface modules	IF 964-DP
<b>Protocols</b>	
<b>SIMATIC communication</b>	
• S7 routing	Yes
<b>Isochronous mode</b>	
Equidistance	Yes
User data per isochronous slave, max.	244 byte
shortest clock pulse	1 ms
max. cycle	32 ms
<b>communication functions / header</b>	
<b>PG/OP communication</b>	
• Number of connectable OPs with message processing	8
• Number of connectable OPs without message processing	31
<b>Global data communication</b>	
• supported	Yes
• Number of GD loops, max.	8
• Number of GD packets, transmitter, max.	8
• Number of GD packets, receiver, max.	16
• Size of GD packets, max.	64 byte
• Size of GD packet (of which consistent), max.	1 variable
<b>S7 basic communication</b>	
• supported	Yes; in MPI mode via: SFC X_SEND, X_RCV, X_GET and X_PUT; in DP master mode via: SFC I_GET and I_PUT
• User data per job, max.	76 byte
• User data per job (of which consistent), max.	1 variable

<b>S7 communication</b>	
• supported	Yes
• as server	Yes
• as client	Yes
• User data per job, max.	64 kbyte
• User data per job (of which consistent), max.	462 byte; 1 variable
<b>S5 compatible communication</b>	
• supported	Yes; Via FC AG_SEND and AG_RECV, max. via 10 CP 443-1 or 443-5
• User data per job, max.	8 kbyte
• User data per job (of which consistent), max.	240 byte
<b>Standard communication (FMS)</b>	
• supported	Yes; Via CP and loadable FB
<b>Number of connections</b>	
• overall	32
• usable for PG communication	
— reserved for PG communication	1
• usable for OP communication	
— reserved for OP communication	1
<b>S7 message functions</b>	
Number of login stations for message functions, max.	8
Symbol-related messages	Yes
Program alarms	Yes
simultaneously active Alarm_S blocks, max.	100; ALARM_S/SQ blocks or ALARM_D/DQ blocks
Alarm 8-blocks	Yes
• Number of instances for alarm 8 and S7 communication blocks, max.	600
• preset, max.	300
Process control messages	Yes
Number of archives that can log on simultaneously (SFB 37 AR_SEND)	16
<b>Number of messages</b>	
• overall, max.	512
• in 100 ms grid, max.	128
• in 500 ms grid, max.	256
• in 1000 ms grid, max.	512
<b>Number of additional values</b>	
• with 100 ms grid, max.	1
• with 500, 1000 ms grid, max.	10
<b>Test commissioning functions</b>	
Status block	Yes
Single step	Yes
Number of breakpoints	4
<b>Status/control</b>	
• Status/control variable	Yes
• Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
• Number of variables, max.	70
<b>Forcing</b>	
• Forcing	Yes
• Forcing, variables	Inputs/outputs, bit memories, distributed I/Os
• Number of variables, max.	256
<b>Diagnostic buffer</b>	
• present	Yes
• Number of entries, max.	3 200
— adjustable	Yes
— preset	120
<b>configuration / header</b>	
<b>Configuration software</b>	
• STEP 7	Yes
<b>configuration / programming / header</b>	
• Command set	see instruction list
• Nesting levels	8

• Access to consistent data in process image	Yes
• System functions (SFC)	see instruction list
• System function blocks (SFB)	see instruction list
<b>Programming language</b>	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— CFC	Yes
— GRAPH	Yes
— HiGraph®	Yes
<b>configuration / programming / number of simultaneously active SFC / header</b>	
— DPSYC_FR	2
— D_ACT_DP	4
— RD_REC	8
— WR_REC	8
— WR_PARM	8
— PARM_MOD	1
— WR_DPARM	2
— DPNRM_DG	8
— RDSYSST	8; 1 to 8
— DP_TOPOL	1
<b>configuration / programming / number of simultaneously active SFB / header</b>	
— RDREC	8
— WRREC	8
<b>Know-how protection</b>	
• User program protection/password protection	Yes
<b>Dimensions</b>	
Width	50 mm
Height	290 mm
Depth	219 mm
<b>Weights</b>	
Weight, approx.	1 070 g

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